

**IN THE CLAIMS:**

Please amend the claims, as follows:

Claim 1. (Currently Amended): A ceramic passive component which comprises a carrier substrate (1),

at least one first electrode (2) formed of a material selected from the group consisting of metals and alloys and having a first surface disposed, directly on the substrate,

at least one thin film dielectric (5) of a thickness in the range of about 0.25-0.75  $\mu\text{m}$  having a first surface disposed, on a second surface of the at least one first electrode opposing said first surface of the at least one first electrode, and

at least one second electrode (6) disposed on a second surface of the at least one dielectric opposing said first surface of the at least one dielectric,

wherein the at least one thin film dielectric (5) comprises a ferroelectric ceramic material with a voltage-dependent relative dielectric constant  $\epsilon_r$ , and

wherein the ferroelectric ceramic material with a voltage-dependent dielectric constant  $\epsilon_r$  is a material selected from the group consisting of:

$\text{Ba}_{1-x}\text{Sr}_x\text{TiO}_3$  ( $0 \leq x \leq 1$ ) ( $0 \leq x \leq 1$ )  $\text{Pb}_{1-1.5y}\text{La}_y(\text{Zr}_x\text{Ti}_{1-x})\text{O}_3$  ( $0 \leq x \leq 1$ ,  $0 \leq y \leq 0.2$ ),  $\text{Pb}(\text{Zr}_x\text{Ti}_{1-x})\text{O}_3$  ( $0 \leq x \leq 1$ ) doped with Nb,  $\text{Pb}_{1-\alpha y}\text{La}_y\text{TiO}_3$  ( $0 \leq y \leq 0.3$ ,  $1.3 \leq \alpha \leq 1.5$ ), (Pb, Ca)  $\text{TiO}_3$ ,  $\text{BaTiO}_3$  with and without dopants,  $\text{SrZr}_x\text{Ti}_{1-x}\text{O}_3$  ( $0 \leq x \leq 1$ ) with and without Mn dopants,  $\text{BaZr}_x\text{Ti}_{1-x}\text{O}_3$  ( $0 \leq x \leq 1$ ),  $\text{SrTiO}_3$  doped with, for example, La, Nb, Fe or Mn,

$(\text{Pb}(\text{Mg}_{1/3}\text{Nb}_{2/3})\text{O}_3)_x - (\text{PbTiO}_3)_{1-x}$  ( $0 \leq x \leq 1$ )

$(\text{Pb, Ba, Sr}) (\text{Mg}_{1/3}\text{Nb}_{2/3})_x\text{Ti}_y(\text{Zn}_{1/3}\text{Nb}_{2/3})_{1-x-y}\text{O}_3$  ( $0 \leq x \leq 1$ ,  $0 \leq y \leq 1$ ,  $x + y \leq 1$ ),

$\text{PbNb}_{4/5x}((\text{Zr}_{0.6}\text{Sn}_{0.4})_{1-y}\text{Ti}_y)_{1-x}\text{O}_3$  ( $0 \leq x \leq 0.9$ ,  $0 \leq y \leq 1$ ),

$(\text{Ba}_{1-x}\text{Ca}_x)\text{TiO}_3$  ( $0 \leq x \leq 1$ ),

$(\text{Ba}_{1-x}\text{Sr}_x)\text{TiO}_3$  ( $0 \leq x \leq 1$ ),  $(\text{Ba}_{1-x}\text{Pb}_x)\text{TiO}_3$  ( $0 \leq x \leq 1$ ),  $(\text{Ba}_{1-x}\text{Sr}_x)(\text{Ti}_{1-x}\text{Zr}_x)\text{O}_3$  ( $0 \leq x \leq 1$ ,  $0 \leq y \leq 1$ )

- (a)  $\text{Pb}(\text{Mg}_{1/2}\text{W}_{1/2})\text{O}_3$ ,
- (b)  $\text{Pb}(\text{Fe}_{1/2}\text{Nb}_{1/2})\text{O}_3$ ,
- (c)  $\text{Pb}(\text{Fe}_{2/3}\text{W}_{1/3})\text{O}_3$ ,
- (d)  $\text{Pb}(\text{Ni}_{1/3}\text{Nb}_{2/3})\text{O}_3$ ,
- (e)  $\text{Pb}(\text{Zn}_{1/3}\text{Nb}_{2/3})\text{O}_3$
- (f)  $\text{Pb}(\text{Sc}_{1/2}\text{Ta}_{1/2})\text{O}_3$ ,

as well as combinations of any of the materials (a) to (f) with  $\text{PbTiO}_3$  and  $\text{Pb}(\text{Mg}_{1/3}\text{Nb}_{2/3})\text{O}_3$  with and without excess lead.

Claim 2. (Cancelled)

Claim 3. (Previously presented) A ceramic passive component as claimed in claim 1, wherein the at least one first electrode (2) or the at least one second electrode (6) comprise(s) at least a first and a second electrically conducting layer.

Claim 4. (Previously presented) A ceramic passive component as claimed in claim 3, wherein the first electrically conducting layer of the at least first electrode(2) or of the at least one second electrode (6) comprises Ti, Cr,  $\text{Ni}_x\text{Cr}_y$  ( $0 \leq x \leq 1$ ,  $0 \leq y \leq 1$ ) or  $\text{Ti}_x\text{W}_y$  ( $0 \leq x \leq 1$ ,  $0 \leq y \leq 1$ ).

Claim 5. (Previously presented) A ceramic passive component as claimed in claim 3, wherein the second electrically conducting layer of the at least one first electrode (2) or of the at least one second electrode (6) comprises a metal or an alloy.

Claim 6. (Previously presented) A ceramic passive component as claimed in claim 1, wherein the carrier substrate (1) comprises a ceramic material, a ceramic material with a glass planarization layer, a glass-ceramic material, a glass material, or silicon.

Claim 7. (Previously presented) A ceramic passive component as claimed in claim 1, wherein the at least one dielectric (5) comprises multiple layers.

Claim 8. (Previously presented) A ceramic passive component as claimed in claim 1, wherein a protective layer (7) is laid over the entire component.

Claim 9. (Currently amended) A voltage-controlled oscillator comprising a capacitive component comprising a ceramic passive component which comprises a carrier substrate (1), at least one first electrode (2) formed of a material selected from the group consisting of metals and alloys and having a first surface disposed directly on the substrate, at least one thin film dielectric (5) of a thickness in the range of about  $[[0.25-0.75\text{mm}]]$   $0.25-0.75\mu\text{m}$  having a first surface disposed, on a second surface, opposed to said first surface of the at least first electrode, and at least a second electrode (6) disposed on a second surface of the at least one thin film dielectric, opposed to said first surface of the at least one dielectric, wherein the at least on thin film dielectric (5) comprises a ferroelectric ceramic material with a voltage-dependent relative dielectric constant  $\epsilon_r$ , wherein said capacitive component is mounted with other components of said voltage-controlled oscillator.

Claim 10. (Currently amended) A filter comprising a capacitive component comprising a ceramic passive component which comprises a carrier substrate (1), at least one first electrode (2) formed of a material selected from the group consisting of metals and alloys and having a first surface disposed directly on the substrate, at least one thin film dielectric (5) of a thickness in the range of about  $[[0.25-0.75\text{mm}]]$   $0.25-0.75\mu\text{m}$  having a first surface disposed on a second surface of the at least one first electrode (2) opposed to the first surface and at least one second electrode having a surface disposed on said second surface of the at least one thin film dielectric wherein the at least on thin film dielectric (5) comprises a ferroelectric ceramic material with a voltage-dependent relative dielectric constant  $\epsilon_r$ , wherein said capacitive component is mounted with other components of said filter.

Claim 11. (Currently amended) A delay line comprising a capacitive component comprising a ceramic passive component which comprises a carrier substrate (1), at least one first electrode formed of a material selected from the group consisting of metal and alloys and (2) having a first surface disposed directly on the substrate at least one thin film dielectric (5) of a thickness in the range of about 0.25-0.75  $\mu\text{m}$  having a first surface disposed on a second surface of the at least one first electrode opposed to said first surface and at least one second electrode (6) having a surface disposed on said second surface of the at least one thin film dielectric wherein the at least one thin film dielectric (5) comprises a ferroelectric ceramic material with a voltage-dependent relative dielectric constant  $\epsilon_r$ , wherein said capacitive component is mounted with other components of said delay line.

Claim 12. (Currently amended) A component with a tunable capacitance comprising:

a capacitive ceramic comprising a carrier substrate (1), at least one first electrode (2) formed of a material selected from the group consisting of metals and alloys and having a first surface disposed directly on the substrate, at least one dielectric (5) of a thickness in the range of about 0.25-0.75  $\mu\text{m}$  with a voltage-dependent relative dielectric constant  $\epsilon_r$ , having a second surface opposed to said first surface disposed on a second surface of the at least one first electrode opposed to said first surface and at least one second electrode (6) disposed on said second surface of the at least one thin film dielectric as a capacitive component, wherein said capacitive ceramic is mounted with other components of said component with a tunable capacitance.